

Day : Friday
Date: 5/11/2007

PALM INTRANET

Time: 19:01:04

Inventor Name Search Result

Your Search was:

Last Name = KNOTTS

First Name = THOMAS

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09539490</u>	<u>6586283</u>	150	03/30/2000	APPARATUS AND METHOD FOR PROTECTING INTEGRATED CIRCUIT CHARGE STORAGE ELEMENTS FROM PHOTO-INDUCED CURRENTS	KNOTTS, THOMAS A.
<u>10601993</u>	<u>6937069</u>	150	06/23/2003	SYSTEM AND METHOD FOR DEAD-BAND DETERMINATION FOR ROTATIONAL FREQUENCY DETECTORS	KNOTTS, THOMAS A.
<u>10714037</u>	Not Issued	30	11/14/2003	Programmable frequency detector for use with a phase-locked loop	KNOTTS, THOMAS A.
<u>10771738</u>	Not Issued	41	02/04/2004	Method and apparatus to enhance contrast in electro-optical display devices	KNOTTS, THOMAS A.
<u>10967824</u>	<u>7095259</u>	150	10/18/2004	REDUCING METASTABLE-INDUCED ERRORS FROM A FREQUENCY DETECTOR THAT IS USED IN A PHASE-LOCKED LOOP	KNOTTS, THOMAS A.
<u>07108850</u>	<u>4801996</u>	150	10/14/1987	GIGAHERTZ RATE INTEGRATED CIRCUIT PACKAGE INCORPORATING SEMICONDUCTIVE MIS POWER-LINE SUBSTRATE	KNOTTS, THOMAS A.
<u>07810946</u>	<u>5166959</u>	150	12/19/1991	PICOSECOND EVENT TIMER	KNOTTS, THOMAS A.
<u>08854429</u>	<u>5841325</u>	150	05/12/1997	FULLY-INTEGRATED HIGH-SPEED INTERLEAVED VOLTGE- CONTROLLED RING OSCILLATOR	KNOTTS, THOMAS A.
<u>10775960</u>	<u>7030711</u>	150	02/10/2004	CENTERING A MULTI-BAND VOLTAGE CONTROLLED	KNOTTS, THOMAS ALLEN

				OSCILLATOR	
10797964	7053719	150	03/11/2004	CONTROLLING A VOLTAGE CONTROLLED OSILLATOR IN A BANG-BANG PHASE LOCKED LOOP	KNOTTTS, THOMAS ALLEN
11186059	7164322	150	07/21/2005	ESTABLISHING A TUNING SIGNAL WINDOW FOR USE IN CENTERING A MULTI-BAND VOLTAGE CONTROLLED OSCILLATOR	KNOTTTS, THOMAS ALLEN
11356514	7209009	150	02/17/2006	CONTROLLING A VOLTAGE CONTROLLED OSCILLATOR IN A BANG-BANG PHASE LOCKED LOOP	KNOTTTS, THOMAS ALLEN
11548238	Not Issued	19	10/10/2006	METHOD AND APPARATUS FOR INFUSING LIQUID TO A BODY	KNOTTTS, THOMAS ALLEN

Inventor Search Completed: No Records to Display.

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EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1	"10714037".rlan. or ("10".src. and "714037".ap.)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2007/05/10 08:32
S3	30	((THOMAS) near2 (KNOTTS)).INV.	US-PGPUB; USPAT; USOCR	OR	ON	2007/03/07 16:09
S4	49	("5739727" "4777449" "5526129" "5836000" "4334312" "5263187" "5657089" "5963073" "6088401" "4251833" "4360788" "4384491" "4616192" "4916412" "5289506" "5727019" "4313209" "4454607" "4481546" "4862485" "5203032" "5363066" "5389898" "5412353" "5420545" "5465277" "5530406" "5590203" "5621537" "5657359" "5802462" "5808493" "5818302" "5821817" "5822387" "5982836" "6014064" "6016082" "6091931" "6118742" "6169585" "6211925" "6211925" "4336505" "4377871" "4389622" "4394776" "4403342" "4441098" "4477919").pn.	USPAT	OR	ON	2007/03/07 16:23
S7	0	((frequency near detect\$3 FD))&((voltage near control near oscillator) VCO) & counter & controller & (prest\$4 near value)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/07 16:31
S9	961	(frequency near detect\$3)&((voltage near control near oscillator) VCO) & counter & controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 17:39
S13	986	("phase locked loop" "phase-locked loop" PLL) & ((frequency near detect\$3 FD))&((voltage near control near oscillator) VCO) & counter & controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/09 20:47
S14	791	("phase locked loop" "phase-locked loop" PLL) & ((frequency near detect\$3 FD))&((voltage near control near oscillator) VCO) & counter & controller & (preset predetermined)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/07 16:35

EAST Search History

S15	286	("phase locked loop" "phase-locked loop" PLL) & ((frequency near detect\$3 FD))&((voltage near control near oscillator) VCO) & counter & controller & ((preset predetermined) near value)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/07 16:35
S16	102	("phase locked loop" "phase-locked loop" PLL) & ((frequency near detect\$3 FD))&((voltage near control near oscillator) VCO) & counter & controller & ((preset predetermined) near value) & ((preset predetermined) near signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/07 16:37
S17	71	("phase locked loop" "phase-locked loop" PLL) & ((frequency near detect\$3 FD))&((voltage near control near oscillator) VCO) & counter & controller & ((preset predetermined) near value) & ((preset predetermined) near signal) & ((reference system main) near clock)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/08 08:33
S22	17	(deadband (daed adj band)) near logic	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/07 16:45
S23	1	S17 and S22	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/07 16:45
S25	6	S22 and ("phase locked loop" "phase-locked loop" PLL)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/07 21:03
S26	6	((deadband (daed adj band)) near logic) and ("phase locked loop" "phase-locked loop" PLL)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/08 08:10
S27	5014	((327/147) or (327/156) or (455/260) or (375/376)).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/08 08:09

EAST Search History

S28	187	(deadband (daed adj band)) and ("phase locked loop" "phase-locked loop" PLL)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/08 08:11
S29	71	("phase locked loop" "phase-locked loop" PLL) & ((frequency near detect\$3 FD))&((voltage near control near oscillator) VCO) & counter & controller & ((preset predetermined) near value) & ((preset predetermined) near signal) & ((reference system main) near clock)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/08 09:25
S30	15	S29 & (counter with VCO)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/08 09:27
S31	32	("phase locked loop" "phase-locked loop" PLL) & (frequency near detect\$3) & ((voltage near control near oscillator) VCO) & counter & controller & ((preset predetermined) near value) & ((preset predetermined) near signal) & ((reference system main) near clock)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/08 09:26
S32	15	S31 & (counter with VCO)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/08 09:27
S33	9122	("phase locked loop" "phase-locked loop" PLL) & ((frequency near detect\$3 FD))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/09 20:48
S34	4	("phase locked loop" "phase-locked loop" PLL) & ((frequency near detect\$3 FD)) & counter & controller & (prest\$4 near value)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 17:37
S37	5394	((375/376) or (331/17) or (455/76) or (331/14) or (331/1A) or (348/729)).CCLS.	USPAT; USOCR	OR	OFF	2007/05/10 08:33

EAST Search History

S41	49	("5739727" "4777449" "5526129" "5836000" "4334312" "5263187" "5657089" "5963073" "6088401" "4251833" "4360788" "4384491" "4616192" "4916412" "5289506" "5727019" "4313209" "4454607" "4481546" "4862485" "5203032" "5363066" "5389898" "5412353" "5420545" "5465277" "5530406" "5590203" "5621537" "5657359" "5802462" "5808493" "5818302" "5821817" "5822387" "5982836" "6014064" "6016082" "6091931" "6118742" "6169585" "6211925" "6211925" "4336505" "4377871" "4389622" "4394776" "4403342" "4441098" "4477919").pn.	USPAT	OR	ON	2007/05/10 17:33
S42	5394	((375/376) or (331/17) or (455/76) or (331/14) or (331/1A) or (348/729)).CCLS.	USPAT; USOCR	OR	OFF	2007/05/10 17:33
S43	25	S42 & S41	USPAT	OR	OFF	2007/05/10 17:34
S44	4779	("phase locked loop" "phase-locked loop" PLL) & (frequency near detector)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 17:38
S45	690	(frequency near detector)&((voltage near control near oscillator) VCO) & counter & controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 17:39
S46	214	(frequency near detector)&((voltage near control near oscillator) VCO) & (counter with controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 17:41
S47	36	(frequency near detector)&((voltage near control near oscillator) VCO) & (counter with controller) & (VCO with controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 17:41